

MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT	
Peak Repetitive Reverse Voltage				
and DC Blocking Voltage	V _{RRM}	100	V. H.	
SSR2010CT	V _{RWM}	100	Volts	
	V _R			
Average Rectified Output Current ^{1/}				
(Resistive Load, 60Hz, Sine Wave, TA=25°C)	Io	20	Amps	
Peak Surge Current ^{1/}				
(8.3 ms Pulse, Half Sine Wave superimposed on I_0 , allow junction to reach equilibrium between pulses, TA=25 °C)	I _{FSM}	300	Amps	
Operating and Storage Temperature	T _{OP} & T _{STG}	-65 to +175	°C	
Maximum Thermal Resistance ^{1/}				
Junction to Case	$\mathbf{R}_{\mathbf{ hetaJC}}$	0.8	°C/W	

Note: <u>1</u>/ Both Legs Tied Together.

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.	DATA SHEET #: RS0067D	DOC
---	-----------------------	-----



SSR2010CT

ELECTRICAL CHARACTERISTICS (Per Leg)					
CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT		
Instantaneous Forward Voltage Drop ($I_F = 3 \text{ Adc}, T_A = 25 \text{ °C}, \text{Pulse}$) ($I_F = 5 \text{ Adc}, T_A = 25 \text{ °C}, \text{Pulse}$) ($I_F = 10 \text{ Adc}, T_A = 25 \text{ °C}, \text{Pulse}$)	V_{F1} V_{F2} V_{F3}	0.68 0.7 0.77	Vdc		
Instantaneous Forward Voltage Drop $(I_F = 5 \text{ Adc}, T_A = -55 \text{ C}, \text{ Pulse})$	V_{F4}	0.84	Vdc		
Reverse Leakage Current (Rated V_R , $T_A = 25$ °C, Pulse)	I _{R1}	100	μΑ		
Reverse Leakage Current (Rated V_R , $T_A = 100^{\circ}$ C, Pulse)	I _{R2}	5	mA		
Junction Capacitance ($V_R = 10 \text{ Vdc}, T_A = 25^{\circ}\text{C}, f = 1 \text{ MHz}$)	CJ	400	pF		

Case Outline- SMD1

PIN OUT: PIN 1- ANODE 1 PIN 2- CATHODE PIN 3- ANODE 2

